Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	26609	((bit vector) same ((generat\$4 creat\$4 provid\$4 insert\$4 yield\$4 produc\$3) instruction)).clm.	US-PGPUB	OR	OFF	2006/12/07 09:59
L2	83	((bit adj vector) same ((generat\$4 creat\$4 provid\$4 insert\$4 yield\$4 produc\$3) instruction)).clm.	US-PGPUB	OR	OFF	2006/12/07 09:59
L3	17	((bit adj vector) same ((code call instruction function method) near6 (dynamically runtime generat\$4))). clm.	US-PGPUB	OR	OFF	2006/12/07 10:00
L4	17	2 and 3	US-PGPUB	OR	OFF	2006/12/07 10:03
L5	1	(bit near vector).clm. and emulat\$4.clm.	US-PGPUB	OR	OFF	2006/12/07 10:04

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S13 0	31	(US-5668999-\$ or US-5740441-\$ or US-6119206-\$ or US-6014723-\$ or US-5835958-\$ or US-6996677-\$ or US-5301325-\$ or US-6532531-\$ or US-5623614-\$ or US-5187799-\$ or US-6157999-\$ or US-5507030-\$ or US-5598560-\$ or US-5627998-\$ or US-5930509-\$ or US-5950003-\$ or US-6091897-\$ or US-5790778-\$ or US-6088779-\$ or US-6408379-\$ or US-6125439-\$ or US-533998-\$ or US-5416912-\$ or US-5134693-\$). did. or (US-5628016-\$ or US-3878513-\$).did. or (US-6091897-\$).did.	USPAT; DERWENT	OR	OFF	2006/12/01 16:12
S13 1	117	((check\$4 boundaries limit threshold) near6 (memory resource stack)) and (((first one) and (another second)) near4 (OS platform architecture)) and (emulation emulat\$4 simulat\$4)	USPAT; DERWENT	OR	OFF	2006/12/01 16:16
S13 2	28	S131 and (bit near vector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:22
S13 3	27	S132 and ((resource memory stack) near8 check\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:24
S13 4	87 [°]	(Yates.in. "ATI".as.) and emulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:22
S13 5	12	S134 and S131	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:24

S13 6	12	S134 and "bit vector" and ((resource memory stack) near8 check\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:39
S13 7	0	S136 and instrument\$5 and ((command directive instruction) near4 (prologue trampoline insert\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:29
S13 8	0	S136 and instrument\$5 and ((command directive instruction) and (instrument\$4 added prologue trampoline insert\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:30
S13 9		S136 and instrument\$5 same ((command directive instruction) or (added prologue trampoline insert\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:30
S14 0	12	S136 and ((command directive instruction) or (added prologue trampoline insert\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:31
S14 1	12	S140 and ((stack resource memory allocation) near5 (check\$4 verif\$4 validat\$4 determin\$4) same (instruction code command byte))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:53
S14 2	2664	emulat\$4 and ((stack resource memory allocation) near5 (check\$4 verif\$4 validat\$4 determin\$4) same (instruction code command byte))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:38
S14 3	527	S142 and ((insert\$4 instrument\$5 prologue) near6 (instruction code command routine))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:39

S14 4	527	S143 and ((stack resource memory allocation) near5 (check\$4 verif\$4 validat\$4 determin\$4) same (instruction code command byte))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:39
S14 5	69	S144 and "bit vector" and ((resource memory stack) near8 check\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:40
S14 6	13	(@ad<="19991208" or @rlad<="19991208") and S145	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 16:41
S14 7	2	S146 not S141	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 16:42
S14 8	0	(bit adj vector) near5 (creat\$4 generat\$4) near9 (on-the-fly dynamically runtime)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 17:06
S14 9	0	(bit adj vector) near5 (creat\$4 generat\$4) near9 ("during execution" on-the-fly dynamically runtime)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 16:52
S15 0	0	(bit adj vector) near5 (creat\$4 generat\$4) near9 ("during execution" on-the-fly dynamically runtime (run adj time))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 16:52
S15 1	19	(bit near3 (table map store structure)) near5 (creat\$4 generat\$4) near9 ("during execution" on-the-fly dynamically runtime (run adj time))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 16:52
S15 2	1	S151 and ((stack resource memory allocation) near5 (check\$4 verif\$4 validat\$4 determin\$4) same (instruction code command byte))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/01 16:53

S15 3	0	(bit near9 ANDed) near5 (creat\$4 generat\$4) near9 (on-the-fly dynamically runtime)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 17:07
S15 4	34	(bit near9 (compared "ANDed")) and S131	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 17:08
S15 5	23	S154 and (bit near3 (map vector))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/01 17:08
S15 6	0	(instrumentat\$4 debug\$4 emulat\$4) and (bit adj vector) same ((stack memory) with ((resource near4 available) exceed\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/06 14:34
S15 7		(instrumentat\$4 debug\$4 emulat\$4) and (bit adj vector) and ((stack memory) with ((resource near4 available) exceed\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/06 14:34
S15 8	6	S157 and ((generat\$4 creat\$4) with (bit near vector))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/06 14:35

tuanvu2112@hotmail.com | My Account | Sign out

Google

Neb <u>Images Video News Maps more »</u>

dynamically generating bit vector emulation Search Advanced Search Preferences

Web

Results 1 - 10 of about 351,000 for <u>dynamically generating bit vector emulation</u>. (0.45 seconds)

Analog Devices: Embedded Processing & DSP: Technical Support ... 28244: Issue with dynamically created VDK components at startup ... Tool: Emulator. Prognosis: No plans to Fix. JTAG scan errors generated while reading ... www.analog.com/processors/cda/epTASearchResult/0,3001,,00.html - 961k - Cached - Similar pages

Title Index

1 · Information Technology — 8-bit single-byte coded graphic character sets — Part 5: Latin/Cyrillic ... Generated by sharef2html on 2006-11-10, 08:08:47. dret.net/biblio/titles - 957k - Cached - Similar pages

Title Index

ARPA Assignment Procedures · **Dynamic** Delegation Discovery System (DDDS) Part Four: The Uniform Resource ... **Generated** by sharef2html on 2006-11-26, 06:01:23. dret.net/rfc-index/titles - Similar pages

[PDF] Dynamic Cache Partitioning via Columnization 1999, November ...

File Format: PDF/Adobe Acrobat - View as HTML

a new **bit vector** into the mapping table, hardware automatically ANDs this value with ... **emulation** of scratchpad memory is superior to scratchpad memory, ... csg.lcs.mit.edu/pubs/memos/Memo-430/memo-430.pdf - <u>Similar pages</u>

[PDF] Wabi Cpu Emulation

File Format: PDF/Adobe Acrobat - View as HTML

Dynamically generated app code, e.g. video, optimized ... (mmu **emulation**) (flat register model). –. 16-**bit** literal instruction operands ... www.hotchips.org/archives/hc8/2_Mon/HC8.S2/HC8.2.1.pdf - <u>Similar pages</u>

Software As She's Developed » Blog Archive » Ajax/Javascript: 8 ...

Also, there's the High Performance JavaScript **Vector** Graphics Library by Walter Zorn which ... Instead of using JavaScript to **generate** the **dynamic** SVG, ... softwareas.com/ajaxjavascript-8-ways-to-create-graphics-on-the-fly - 61k - Cached - Similar pages

[PDF] Dynamic Cache Partitioning via Columnization

File Format: PDF/Adobe Acrobat - View as HTML

a new **bit vector** into the mapping table, hardware automatically ANDs ... memo table key is used to **generate** a restricted address while the data is stored at ... www.ai.mit.edu/projects/ntt/projects/MIT9904-04/documents/memo-430.pdf - Similar pages

Fink - Package Database

... autoconf2.54 - System for **generating** configure scripts; autoconf25 - System for ... **bit-vector**-pm581 - Efficient base class implementing **bit** vectors ... pdb.finkproject.org/pdb/list.php - 721k - <u>Cached</u> - <u>Similar pages</u>

[PDF] A Dynamic Emulator for Power System Control Applications ...

File Format: PDF/Adobe Acrobat

It is crucial for an **emulator** to be able to **generate** signals with. different waveform. ... timer has three independent 16-**bit** counters, each capable of ... ieeexplore.ieee.org/iel3/3852/11244/00548135.pdf?arnumber=548135 - <u>Similar pages</u>

Syn68k White Paper

Syn68k: ARDI's **dynamically** compiling 68LC040 **emulator** Mat Hostetter ... file describing the **bit** patterns and semantics of the 680x0 instruction set and ... www.ardi.com/synpaper.php - 26k - <u>Cached</u> - <u>Similar pages</u>

Result Page: 1 <u>2</u> <u>3</u> <u>4</u> <u>5</u> <u>6</u> <u>7</u> <u>8</u> <u>9</u> <u>10</u> **Next**

Try Google Desktop: search your computer as easily as you search the web.

dynamically generating bit vector en Search

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

Google Home - Advertising Programs - Business Solutions - About Google

©2006 Google